Ridgetop Group, Inc.
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Job Description: Hardware Design Engineer

Position Overview

The Hardware Design Engineer will be a team member of the technical staff that helps design and develop electronic solutions for Ridgetop Group's customers and projects. The Hardware Design Engineer will play a key role in helping to scale the circuit design work from past, current, and future internal research and development projects to commercial ready products. Customer challenges include short development schedules, evolving requirements, and dynamic operating environments that often require customized solutions for government, commercial, and industrial applications.

Responsibilities

- Must be well versed in circuit design, electrical engineering, and systems engineering
- Proficient in Schematic and PCB design tools such as Eagle, Multisim, Ultiboard, etc.
- Proficient in signal processing techniques for analog, digital, and RF circuitry
- The Hardware Design Engineer shall be a self-motivated problem solver

Qualifications and Requirements

- Bachelor's degree in Electrical Engineering or other equivalent field of discipline
- 3 or more years of PCB and hardware design experience
- Experience generating schematics for high-performance and mixed-signal circuit boards
- Experience guiding and support all aspects of PCB layout, fabrication, and assembly processes
- Ability to perform high-level system design
- Experience with integration, verification, and timing analysis of digital logic and circuitry
- Experience with RF and analog circuit design, as well as RF test equipment
- Ability to contribute to independent research and development (IRAD) and rapid prototyping circuit board designs for single use case applications

Additional Preferred Experience and Skills

- Master's degree Electrical Engineering or other equivalent field of discipline
- 3-5 years of PCB and hardware design experience
- Experience with Engineering Design Software including National Instruments Circuit Design Suit (Multisim & Ultiboard)
- Experience implementing signal processing and communications algorithms for FPGA systems, microprocessors, and developing IoT-based sensing solutions

Position Type / Expected Hours of Work

This Full-Time Employment opportunity is expected to maintain 40 hours per week during regular business hours.

Benefits and Compensation

For Full-Time Employees the Company offers a competitive salary and comprehensive benefits program, including health/dental insurance and an employer match to retirement fund. Compensation is DOE.

Job Location

Corporate Headquarters, Tucson, Arizona.