

## Mixed Signal Design Engineer

Ridgetop Group seeks an experienced Mixed Signal Design Engineer to contribute to the research and development of innovative integrated circuit solutions at its headquarters in Tucson, Arizona. Potential applications include high performance data converters and radiation-hardened power management ASICs. This position requires participation in all aspects of development including product specification, floor planning, transistor level design, layout supervision, chip level verification, and device characterization.

### Responsibilities

- Contribute to the design and layout of mixed signal integrated circuits
- Perform layout verification tasks, including LVS and DRC
- Provide version control for PDKs and cell libraries
- Assist with final tape-out, device assembly, and silicon validation
- Support process improvement activities both within the department and company-wide
- Work both independently and in a team environment, with the opportunity to provide technical leadership to other members of the engineering team
- Create and/or modify specification documents detailing system design and enhancements to meet marketing requirements
- Collaborate with others in the creation of technical reports, whitepapers, and user documentation
- Provide on-going client support and maintenance

### Requirements

- EE degree with at least 5 years of design and/or test engineering experience
- Proven experience taking designs from concept to production
- Strong knowledge of analog integrated circuit design fundamentals
- Transistor and system level simulation skills
- Discrete time and continuous time signal processing skills
- Strong lab and silicon validation skills
- Strong communication skills, both written and verbal
- Strong time management, task prioritization, and interpersonal skills
- Ability to multi-task and work independently as well as part of a team
- Ability to rapidly grasp technical concepts and solutions
- High level of personal drive and self-motivation
- Embedded systems design and firmware development experience is desirable
- Language-based digital design and test bench development is a plus, particularly with Verilog

**Due to the nature of the projects we work on, US citizenship or legal permanent US residency status is required.**

If you are interested in joining Ridgetop, an innovative small company in Tucson, Arizona, and meet the above criteria, please send a resume and cover letter to: [jobs@ridgetopgroup.com](mailto:jobs@ridgetopgroup.com).

Ridgetop Group, Inc. is an equal opportunity employer.