



Ridgetop Group, Inc.
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ADC Design Internship Tucson, AZ

For this position, the Intern will assist Ridgetop's IC Design Staff perform a range of mixed-signal design tasks related to high-performance data converter technologies. Based in Tucson, Ridgetop Group, Inc. is the world leader in providing electronic prognostics (ePHM) and semiconductor solutions to aerospace, automotive and semiconductor firms. The internship can be for summer only or can continue in the fall. The work schedule can be flexible and part-time based on the school schedule.

It is expected that the Intern will work closely with the Ridgetop design team and the responsibilities will include:

1. Assisting in high-speed, silicon-germanium-based BiCMOS operational trans-conductance amplifier design
2. Assisting in track-and-hold stage design
3. Assisting in pipeline analog-to-digital converter (ADC) design
4. Assisting in digital calibration circuitry design
5. Assisting in physical chip layout design
6. Assisting in preparation of status reports and presentations

Background Required

1. Engineering student in good-standing at the University (MSc/PhD preferred)
2. Strong analog IC design skills
3. Letter of recommendation from a Faculty Member
4. Must demonstrate strong initiative with ability to work independently and as part of a team.
5. Excellent oral, written and interpersonal communications skills.
6. Ability to function well as a key contributor in a small group environment is important.

Please send your resume to:

jobs@ridgetop-group.com

Ridgetop Group, Inc. is an Equal Opportunity Employer.

Compensation

Competitive compensation with an established hourly rate.